

FIG.2

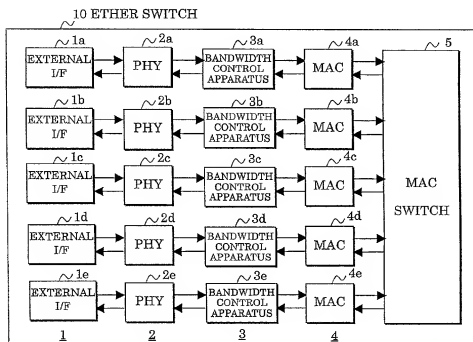


FIG.4

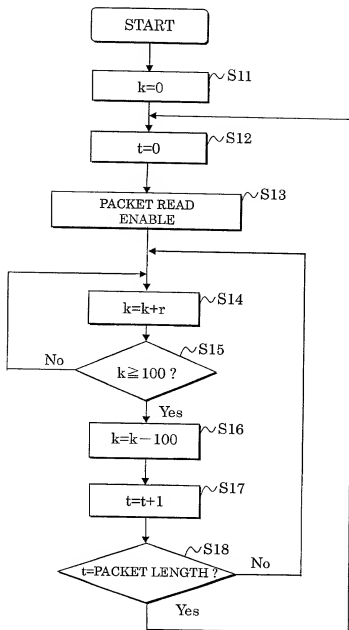


FIG.5

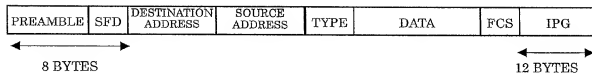


FIG.6A 50 Mbps

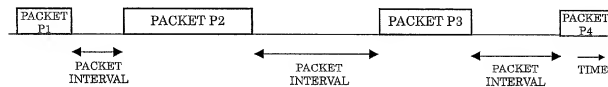


FIG.6B 10 Mbps

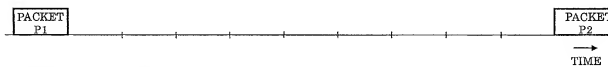


FIG.6C 99 Mbps

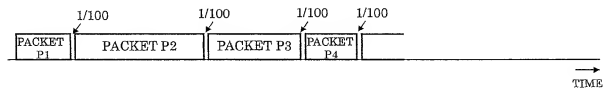


FIG.6D 100 Mbps

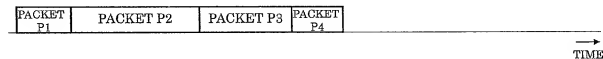


FIG.7A

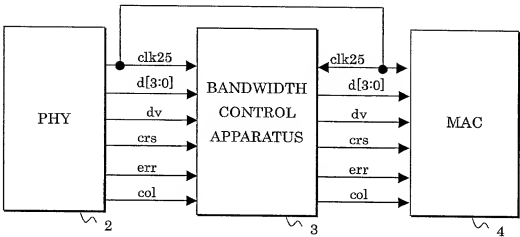


FIG.7B

MII INTERFACE		
SIGNAL NAME	INPUT/ OUTPUT	EXPLANATION
clk25	i	CLOCK (25MHz)
d[3:0]	i	DATA
dv	i	DATA VALID (ENABLE)
crs	i	CARRIER SENSE (ONLY RX)
err	i	DATA ERROR (ONLY RX)
col	i	COLLISION DETECTION (ONLY RX.UNUSED)